

The University of Electro-Communications

Job Openings: Teaching and Research Staff

(The field of Embedded Hardware)

1. Title and number of position

Associate Professor or Tenure Track Assistant Professor, One position

2. Affiliation after the employment

Department of Computer and Network Engineering, Computer Science Program

Graduate School of Informatics and Engineering

3. Fields of specialty

The fields of embedded hardware systems that perform real-time data analysis and system control with AI edge computing by collecting data using sensors, IoT devices, and wireless communication technologies. Research that applies compact and low power circuit design, signal / image processing with edge devices, wireless networks such as LPWA and GPS, and deep learning using GPGPU.

4. Responsible subjects

One or two subjects from physics laboratory, cluster basic and special laboratory, basic subjects, cluster basic and special subjects.

<http://www.uec.ac.jp/campus/academic/syllabus.html>

<http://www.uec.ac.jp/education/undergraduate/curriculum.html>

<http://www.uec.ac.jp/education/undergraduate/cluster1/curriculum.html>

5. Eligibility for application

- 1) Applicants must possess a doctorate degree at the time of adoption.
- 2) Those who have a good track record of the above mentioned fields of specialty, and can contribute to the progress of the fields through activities in education and research.
- 3) Applicants are expected to be capable of guiding students in Japanese.

6. Start of employment

1st October, 2021 (or later at the earliest possible date.)

7. Term of employment and Employment status

- 1) Term of employment:

Associate Professor position: permanent full-time (until the mandatory retirement age)

Assistant Professor position (UEC Tenure Track): Five years from the start of the employment. If the tenure is not granted, maximum of two years may be added to the period of employment in order to provide preparation time for transfer.

Note: Please refer to the following URL for general information on the tenure track program at UEC.

http://www.uec.ac.jp/eng/research/activity/uec_tenure_track/index.html

2) Employment status: Full-time research and education employee

3) Place of employment: The University of Electro-Communications (1-5-1 Chofugaoka, Chofu, Tokyo)

4) Work hours: Discretionary labor system

Based on standard work hours of 7 hours and 45 minutes during 8:30am-5:15pm, an individual employee may decide how his/her schedule will fit into work day.

5) Holidays: Saturday, Sunday, holidays prescribed by the National Holiday Act, New Year's holiday, and the days designated by UEC. However, if an employee teaches a night course of the Fundamental Programs for Advanced Engineering that is offered on Saturday, the day off should be taken on one of the weekdays instead. For securing the number of credit hours, an employee may need to work on public holidays.

6) Paid holidays: Annual paid holidays, special leave, childcare leave, and nursing care leave

7) Insurances: Health insurance/pension (the Monbukagakusho Mutual-Aid Association insurance), employment insurance, and worker's compensation insurance.

8. Salary

Annual salary based on the UEC Employee's Salary Regulation

9. Documents for Submission

1) Application form: (Photo, Resume, List of research achievements)

Please download and fill in the Application Form.

http://www.uec.ac.jp/about/advertisement/pdf/oubo_sinseisyo_e.pdf (PDF file)

http://www.uec.ac.jp/about/advertisement/date/oubo_sinseisyo_e.doc (MS-word file)

2) Research papers (up to 5 papers: duplicates are acceptable)

3) Outline of research achievements and research plan in UEC: up to 4 pages of A4 sheet, relevance to the fields of specialty in section 3 above should be clearly indicated.

4) Teaching experience and statement of resolution to carry out university education: up to 2 pages of A4 sheet

5) Names, affiliations, contacts (including e-mail address) and relation of references:

Your desired position (associate professor or assistant professor)

3 references at least 1 reference in Japan and 1 reference from outside of Japan in the case of the associate professor or 2 references in the case of the assistant professor.

10. Address for submission of application documents

Application documents are not subject to return. The application documents will not be used for purposes other than this selection.

Online submission: Application documents must be uploaded through the following site.

<https://www.inf.uec.ac.jp/kobo/>

If you are having difficulty with online submission, application documents on CD or USB memory stick as PDF files can be sent to the following address by postal mail.

Prof. Fengchao XIAO

Department of Computer and Network Engineering
Graduate School of Informatics and Engineering
The University of Electro-Communications
1-5-1 Chofugaoka, Chofu City, Tokyo 182-8585 Japan

(Please write “Application documents for faculty position in the field of Embedded Hardware” on the envelope in red ink, and it should be sent by Simplified Registered Mail. Also please notify the contact personnel as shown in section 13 by E-mail that you have sent the application documents to us.)

11. Application Deadline

The deadline for submitting the application is 5PM on May 20, 2021 (Japan Standard Time)

12. Selection Procedure

The oral interview will be conducted for only those applicants who have passed their document screening.

Please note that applicants are responsible for the expenses incurred for an interview.

An online interview is available for applicants from outside of Japan.

In case of requesting to undergo an online interview, please contact the following.

13. Contact

Prof. Akashi Satoh

Department of Computer and Network Engineering
Graduate School of Informatics and Engineering
The University of Electro-Communications

E-mail: akashi.satoh@uec.ac.jp

14. Others

- All faculty members of UEC belong to the Academic Institute where individual faculty members are assigned to specific departments or centers.
- Based on an aim to engage in the target value that is described in the 5th Science and Technology Basic Plan in fiscal year 2016–2020 (Cabinet office, Government of Japan, 2016), the government is acting to achieve a ten percent increase in the number of young researchers under 40 years of age, and hopes to increase this number by over thirty percent in the overall higher educations for the future. UEC is promoting the proactive recruitment of young researchers with the goal of fostering those young researchers capable of playing an active role in various field.

For more information, please refer to the following URL:

<https://www8.cao.go.jp/cstp/kihonkeikaku/index5.html>

- To further promote gender equality and diversity measures, UEC endeavors to increase the number of female faculty members through an effort to ensure women-friendly working environment such as activities in consideration of work-life balance and supports for Research Career Advancement. For further information, please refer to the following link.

<http://www.ge.uec.ac.jp/en/>